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DOCKET NO. 00-072
68605(6653)

Amendments to the Claims:

8.(new): A power sequence protection circuit
comprising:

a switch connected to a level shifter between an output
signal port or an inverted output signal port of the level
shifter and a common voltage rail; and

A
a latch connected to the switch to drive the switch to a
conducting state if an input voltage supply is not powered on
when an output voltage supply is powered on and to drive the
switch to a non-conducting state if the input voltage supply
is powered on when the output voltage supply is powered on.

9.(new): The power sequence protection circuit of
Claim 8 wherein the switch comprises two field effect
transistors connected in series.

10.(new): The power sequence protection circuit of
Claim 8 wherein the latch comprises two field effect
transistors connected in series between a third field effect
transistor and the common voltage rail.